

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	12	SCR\$type adj switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:55
S2	1	S1 same unrectified	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:56
S3	1119	scr adj switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:57
S4	4033	thyristor adj switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:57
S5	0	S4 same unrectified	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:58
S6	0	S3 same unrectified	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:58
S7	2	S4 and unrectified	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 17:59
S8	0	S4 same high\$frequency same HF	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:06

S9	11	S4 same hf	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:02
S10	11	S4 same high\$frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:07
S11	53	S4 same (high adj frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:07
S12	0	S11 same perios same insuficient	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:08
S13	0	S11 same periods same insuficient	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:08
S14	1	S11 same periods same insufficient	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/08 18:09
S15	61057	(silicon adj control\$4 adj rectifier) scr (silicon\$control\$4 adj rectifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 10:25
S16	2634	S15 same gate same frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 08:47

S17	120	S16 same (khz mhz ghz)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 08:47
S18	27	S17 same half	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 08:51
S19	87198	triac thyristor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 09:47
S20	2700	S19 same gate same frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 09:47
S21	160	S20 same (khz mhz ghz)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 09:48
S22	30	S21 same half	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 09:48
S23	0	S18 same (insufficient non-sufficient)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 10:16
S24	2	S22 same (sufficient non-sufficeient)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 10:17

S25	140692	(silicon adj control\$4 adj rectifier) scr (silicon\$control\$4 adj rectifier) triac thyristor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 10:26
S26	487	S25 same (insulating adj layer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 10:27
S27	12	S26 same sensitive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 10:29